

AC in

DC in

AC + DC

out

The Model 5575A is a broadband coaxial bias insertion tee and DC blocking capacitor designed to pass fast rise pulses with minimum waveform distortion. The risetime is 30 ps with a -3 dB bandwidth extending from 10 kHz to 12 GHz. The 5575A will safely carry 500 mA of DC current. However, core saturation limits the low frequency response at DC currents above 20 mA. See Notes [1-3].

30 ps, 35 ps max.
12 GHz, 10 GHz min.
10 kHz
$0.6 \text{ dB}, \pm 0.5 \text{ dB}$
50 Ω
±5%, t < 100 ps -6%, t > 100 ps
500 mA max.
> 30 dB
0.1 < f <10 GHz RL >18 dB – 1.2 dB/GHz * f (GHz)
50 V max.
8 mH, ± 30%
-3 dB low freq.
< 10 kHz
70 KHz
300 kHz
0.6 Ω
3.5 W max.
10 kHz typical
SMA jacks (f)
0.02 μF -50%, +80%
1.95" x 0.5" x 1.82"
(5 x 1.3 x 4.6 cm)
One year. See Terms and Conditions of Sale for details

Notes

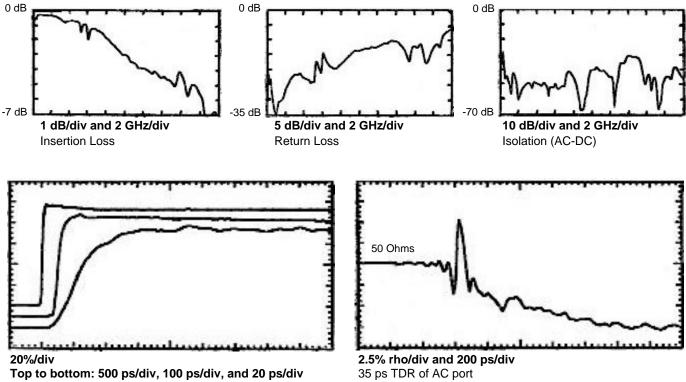
[1] Parameters listed are typical values. They are guaranteed only when maximum and / or minimum limits are given. [2] 10 ps risetime step responses and TDR waveform measured using a PSPL Model 4015B pulse generator and an HP-54124A, 50 GHz, 9.4 ps digital sampling oscilloscope.

[3] Frequency response measured using a Wiltron 5447A, 10 MHz - 20 GHz network analyzer.

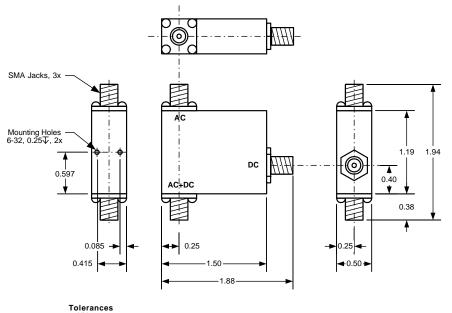
Ordering Information

Model Number
5575A-104





10 ps Step Response



5575A Mechanical Drawing

.XX = .01 .XXX = .005